

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2661 #5 PATENT 5:1

Applicant

Brian Banister

App. No.

09/834,417

Filed

April 12, 2001

For

FAST FEEDBACK CHANNEL

WITH FLEXIBLE BIT

RELIABILITY FOR

WIRELESS

COMMUNICATIONS

Group Art:

2661

Examiner:

Unknown

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

On: March 3, 2004

Barbara S. Kelly

RECEIVED

INFORMATION DISCLOSURE STATEMENT

MAR 0 9 2004

Technology Center 2600

Commissioner for Patents P.O. Box 1450 Washington, DC 22313-1450

Pursuant to 37 C.F.R. §1.56 and in accordance with 37 C.F.R. §§1.97-1.98, information relating to the subject application is hereby disclosed. Inclusion of information in this statement is not to be construed as an admission that this information is material as that term is defined in 37 C.F.R. §1.56(b).

- (X) No additional fee is due because this Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits of the above-identified application, in accordance with 37 CFR §1.97(b).
- (XX) Copies of each of the references listed on the attached modified Form PTO-1449 are enclosed herewith. All of the listed references are in the English language.

Respectfully submitted,

Date: March 3, 2004

Signature Erik M. Vieira Registration No. 53,723

JAQUEZ & ASSOCIATES 750 B Street, Suite 2640 San Diego, CA 92101 (619) 238-1814 (voice) (619) 238-2426 (fax) FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. VIA-014-PAP

APPLICATION NO. 09/834,417

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

SE SEVERAL SHEETS IF NECESSARY)

APPLICANT

Brian Banister

FILING DATE

April 12, 2001

GROUP 2661

				U.S. PATENT DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
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EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)					
	Andrew J. Viterbi, "An Intuitive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes", IEEE Journal on Selected Areas in Communications, Vol. 16, No. 2, February 1998 pp. 260-264.					
	Benedetto, et al. "Serial Concatenation of Interleaved Codes: Performance Analysis, Design, and Iterative Decoding", IEEE Transactions on Information Theory, Vol. 44, No. 3, May 1998, pp. 909-926.					

EXAMINER	
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DATE CONSIDERED